

Anand C. Konnur

S/o C C Konnur, Laxmi Extension, Bombay Chal Road, Gokak- 591307

Tq: Gokak, Dist: Belgaum, State: Karnataka, Nation: India

Ph No: 9620020256 Email ID: anandckonnur1@gmail.com

CAREER OBJECTIVE

To be a part of a dynamic work environment and contribute substantially to the larger goals of an esteemed organization, in the process, making best use of the opportunities provided for learning and growth

ACADEMICS

Academic Year	Name of the College / School	University/ Board	Name of Course	Marks
2013-2015	KLE Dr. M S Sheshgiri College of Engineering and Technology, Belagavi	Visvesvaraya Technological University	Master Of Technology (VLSI Design and Embedded Systems)	72.3 %Aggregate
2007-11	Basaveshwar Engineering College, Bagalkot	Visvesvaraya Technological University	Bachelor of Engineering, (Electronics & Communication Engineering)	7.752 CGPA

Projects Undertaken at College



Title: Implementation of SDRAM Controller for Reconfigurable Platforms Using USB

Duration: 6 Months (as a part of M.Tech Major Project)

Tool Used: Xilinx ISE

Description: Memory controllers will control the data flow in all electronic equipments such as Mobile Phones, Computers, Audio Video devices etc. Large number of signals can be sent or received at a time to the memory device. But sending or receiving the data directly will cause more traffic which degrades the performance of the device. In order to overcome such issues the memory controller is designed. User cannot directly interface with the memory device. In this project we are designing a memory controller acts as an interface to control the signals efficiently between the SDRAM and user. A SDRAM memory device is used in order to decrease the latency and power consumption. The user interface can be done using the USB device.

➤ **Title: Design and Implementation of 7T SRAM cell using cadence**

Duration: 3 months (as a part of I semester M.Tech)

Tool used: Cadence

Description: The project which proposed a 7T SRAM cell that only depends on one of the bit lines during a write operation and reduces the write power consumption. The proposed scheme reduces energy consumption in copying by 92.7% compared to the conventional read-modify-write manner. After using the leakage power reduction technique which name is voltage scaling the waveform of write operation and read operation is quite different from the initial one. The average leakage power consumption after using voltage scaling technique is 7.92% less from the initial one for 7T SRAM cell.

Papers Presented

- Presented a paper titled “**SDRAM Controller Design for Data Acquisition on Xilinx Spartan 3E FPGA**” in IJESRT June, 2015.

Work Experience

KLEDrMSSCET

Asst. Professor

- Involved in teaching for the Dept. of ECE

date

Belagavi
Aug 2015- Till

SLJ Polytechnic

Lecturer

- Involved in teaching for the Dept. of ECE

2013

Gokak
Nov2012–Sep

Mindree Pvt Ltd

Test Engineer

- Involved in manual and automation testing of Kodak Website Bangalore

July2011- Oct 2012

Skills & Interests

- **Programming Languages:** C, C++, Python
- **Hardware Languages:** Verilog
- **Interests:** C++, Operating Systems, Embedded Systems, CMOS VLSI , Computer Networks, C
- **Subjects Handled:** Optical fibre Communications, Management and Entrepreneurship, Programming in C++, Operating Systems,
- **Labs Handled:** Analog electronic circuits Lab, Advanced communications lab, Linear Integrated circuits lab, Microcontroller lab.
- **Areas of Interest in Research:** VLSI Design, Embedded system Networks, Digital Communications and Networking.

Other Achievements & Hobbies

- Conducted workshop on "MSP430 - Advanced Microcontroller", for 4th semester EC/TC students in KLECET, Belgaum in 2014.
- Good communication skills, interpersonal skills and have the ability to work in a team.
- A good cricketer, was selected for VTU Belgaum Zone cricket team.
- Led the BEC cricket for representing VTU and grabbed Runners Up place.
- Students passed with the 100% result for the both subjects I handled in 2015-16.
- Students passed with the 100% result for the both subjects I handled in 2016-17.
- Developed a blog site <https://kleecssa.wordpress.com/> for Dept. of ECE at KLEDrMSSCET Belagavi.
- Effectively handled the 1st year UG students as a Mentor.

Additional Responsibilities

- Have served ECE department as **Sports** coordinator.
- Actively and voluntarily assisted NBA and ISO teams as a service towards administrative responsibilities at KLE.
- Actively assisted **NBA team** and was a part for **certified NBA department** at KLEDrMSSCET Belagavi.
- **Media Club Coordinator** for year 2015-16 and 2016-17.
- **VTU valuation Coordinator** for the year 2016-17.
- Have served as Coordinator for KLE Centenary Celebrations.

Workshops/FDPs Conducted, Organised And Attended

- Attended 1 week FDP on "**Advances in Microcontrollers**" at KLEDRMSSCET Belagavi.
- Attended 1 day workshop on "**Antenna simulation tools**" at KLEDRMSSCET Belagavi.
- Attended 1 day workshop on "**NI tools**" conducted by National instruments.

- Coordinated the SDP on "**Professional Communication and Office Automation**" at KLEDRMSSCET Belagavi.
- Conducted a One Day Workshop on "Thesis Writing Using Lyx for " fro M.tech students at KLEDrMSSCET Belagavi.
- Attended a Two week ISTE STTP on "CMOS, Mixed Signal and Radio Frequency VLSI Design" Conducted by IIT Kharagpur at KLEDrMSSCET Belagavi.

Conferences Attended

- Participated in International Conference on "**Smart Electronic Systems**" at KLEDRMSSCET Belagavi.

Personal Details

Name	: Mr. Anand Konnur
Mother's name	:Mrs.Jayashree Konnur
Father's name	: Mr.Chandrashekhar Konnur
Date of birth	: 27/08/1989
Languages known	: English, Hindi, and Kannada.
Permanent address	: S/o C C Konnur "Anand Nilaya", Laxmi Extn., Bombay Chal Road, Gokak - 591307.
Email-id	: anandckonnur11@gmail.com
Phone no	: +91 9620020256

I, Anand Konnur hereby declare that all the above information furnished by me is true and given an opportunity, I shall definitely prove my worth.

PLACE: Belagavi

Anand Konnur