

RESUME

DEEPTI SHINGHAL

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OBJECTIVE

Nurture the teaching learning process in a Technical Education Institute providing motivation, ambience and exposure to cutting edge technologies.

PRESENT EMPLOYMENT

Present Employer : Moradabad Institute of Technology, Moradabad

Designation : Asst. Prof. (Department of Electronics Engineering)
Since Aug '02.

Total Work Experience : **14 Years**

ACADEMICS

- Pursuing Ph. D. from MONAD University, Hapur, UP (Thesis Submitted)
- M.Tech in “VLSI DESIGN” from “Uttar Pradesh Technical University” Lucknow with 8.17 CPI.
- B.E in “**Electronics & Communication Engineering**” from Moradabad Institute of Technology, M.J.P. Rohilkhand University, Bareilly, with 68% in 2000.
- Passed Intermediate from U. P. Board, with 62% marks in 1996.
- Passed High School from U. P. Board with 58 % marks in 1994.

M.TECH THESIS

- “Design and Implementation of a FAULT TOLERANT MICROPROCESSORS USING VHDL” under the supervision of **Prof. Dinesh Chandra**, Head ECE Dept. J.S.S. Academy of Technical Education, Noida (UP).

PUBLICATIONS

- [1]. SINGLE FAULT TOLERANT MICROPROCESSOR – Deepti Singhal, Amit Saxena, Shuchita Saxena, Design Techniques for modern electronic devices, VLSI and Communication Systems DTVC-2007, NIT Hamirpur, Himachal Pradesh, 14-15 May 2007.
- [2]. HOW TO PROTECT IC DAMAGE FROM ELECTROSTATIC DISCHARGE - Tushar Pandey, Amit Saxena, Deepti Shinghal and Kshitij Shinghal, National Symposium on Emerging Trends in Engineering & Technology (NSETET-07), at MIT, Moradabad, July 20-21, 2007.
- [3]. SYSTEM ON CHIP - Deepti Shinghal, Amit Saxena, Nishant Saxena, and Kshitij Shinghal, National Symposium on Emerging Trends in Engineering & Technology (NSETET-07), at MIT, Moradabad, July 20-21, 2007.
- [4]. SWARM INTELLIGENCE ROBOTICS APPLICATION FOR MOBILE SURVEILLANCE – Amit Saxena, Deepti Shinghal, Kshitij Shinghal and Nishant Saxena, National Symposium on Emerging Trends in Engineering & Technology (NSETET-07), at MIT, Moradabad, July 20-21, 2007.
- [5]. ROBOT SMART VISION SENSORS-A MECHATRONICS APPLICATION- Kshitij Shinghal, Amit Saxena, Nishant Saxena, Deepti Shinghal, Shuchita Saxena, National Conference on advances in Mechanical Engineering, NCAME-2009.
- [6]. SoC DESIGN WITH PROGRAMMABLE LOGIC IP CORES – Deepti Shinghal, Dinesh Chandra, Kshitij Shinghal, Amit Saxena, National conference on Emerging Trends in Embedded Technology, at SGIT, Ghaziabad, February 14, 2009.
- [7]. FAULT TESTING AND DIAGNOSIS OF MICROPROCESSORS - Deepti Shinghal, Dinesh Chandra, Kshitij Shinghal, Amit Saxena, National Conference on Emerging Technologies (NCET-09) at MIT, Moradabad, January 24-25, pp. 249-252.

- [8]. SOC DESIGN WITH FPGAS USING HDLS - Deepti Shinghal, Amit Saxena, Kshitij Shinghal, Shuchita Saxena, Nishant Saxena, National Conference on Emerging Technologies (NCET-09) at MIT, Moradabad, January 24-25, pp. 266-268.
- [9]. SYSTEM-ON-A-CHIP VERIFICATION: A SURVEY - Kshitij Shinghal, Amit Saxena, Deepti Shinghal, Shuchita Saxena, Nishant Saxena, National Conference on Emerging Technologies (NCET-09) at MIT, Moradabad, January 24-25, pp. 275-279.
- [10]. DESIGN OF SYSTEMS ON A CHIP: AN INTRODUCTION - Kshitij Shinghal, Arti Noor, Amit Saxena, Deepti Shinghal, National Conference on Emerging Technologies (NCET-09) at MIT, Moradabad, January 24-25, pp. 280-283.
- [11]. COMPRESSION AND STORAGE OF MEDICAL DATA IN PACEMAKER - Nishant Saxena, Shuchita Saxena, Deepti Shinghal, Amit Saxena, National Conference on Emerging Technologies (NCET-09) at MIT, Moradabad, January 24-25, pp. 339-346.
- [12]. MICROPROCESSOR BASED FAULT TOLERANT MECHATRONICS SYSTEM – Deepti Shinghal, Dinesh Chandra, Kshitij Shinghal, Amit Saxena, National Conference on advances in Mechanical Engineering, NCAME-2009.
- [13]. DESIGN AND ANALYSIS OF A FAULT TOLERANT MICROPROCESSOR BASED ON TRIPLE MODULAR REDUNDANCY USING VHDL - Deepti Shinghal, Dinesh Chandra, International Journal of Advances in Engineering & Technology (IJAET), ISSN: 2231-1963, Mar 2011.
- [14]. ADIABATIC LOGIC CIRCUITS: A RETROSPECT – Deepti Shinghal, Amit Saxena, Dr. Arti Noor, MIT International Journal of Electronics & Communication Engineering (MIT IJ EC), ISSN: 2230-7664, Vol. 3, No. 2, August 2013, pp. 108-114.
- [15]. POWER EFFICIENT ADIABATIC SWITCHING CIRCUITS - Amit Saxena, Deepti Shinghal, Dr. Arti Noor, MIT International Journal of Electronics & Communication Engineering (MIT IJ EC), ISSN: 2230-7664, Vol. 3, No. 2, August 2013, pp. 98-103.
- [16]. COMPARITIVE ANALYSIS OF CONVENTIONAL CMOS & ADIABATIC LOGIC GATES, Amit Saxena, Deepti Shinghal, Dr. Arti Noor, MIT International Journal of Electronics & Communication Engineering (MIT IJ EC), vol 4, No.1,2014.
- [17]. LOW POWER ADIABATIC SWITCHING CIRCUITS: A REVIEW- Deepti Shinghal, Amit Saxena, Dr. Arti Noor, Kshitij Shinghal, International Conference on Advances in Electrical Electronics & Computer Engineering (ICAEECE 2014) at MIT, Moradabad, March 8-9 ,2014, pp. 13.
- [18]. COMPARATIVE ANALYSIS OF ADIABATIC NAND GATE – Deepti Shinghal, Pragati Gupta, Astha, Devendra Singh, Komal Sharma, International Conference on

- Advances in Electrical Electronics & Computer Engineering (ICAEECE 2014) at MIT, Moradabad, March 8-9 ,2014, pp. 36.
- [19]. A REVIEW OF ENERGY DISSIPATION FOR ADIABATIC SWITCHING OF CMOS BASED LOGIC CIRCUITS, Amit Saxena, Deepti Shinghal, Arti Noor, Kshitij Shinghal, ICAEECE-2014, March 8-9, 2014.
- [20]. DESIGN & IMPLEMENTATION OF ADIABATIC BASED LOW POWER LOGIC CIRCUITS, Amit Saxena, Deepti Shinghal, Kshitij Shinghal, International Research Journal of Engineering & Technology(IRJET), Vol 2, Issue 2, 2015.
- [21]. DESIGN & IMPLEMENTATION OF ADIABATIC LATCH FOR LOW POWER EMBEDDED SYSTEM, Deepti Shinghal, A. N. Mishra, Amit Saxena, International Journal of Scientific Research and Management Studies (IJSRMS), Vol 2, Issue4, 2015.
- [22]. LOW POWER ARCHITECTURE FOR ASIP's: BASED ON ADIABATIC SWITCHING PRINCIPLES, Deepti Shinghal, A. N. Mishra, Farooq Hussain, Amit Saxena, International Journal of Engineering Sciences & Emerging Technologies, Vol 8, Issue 6, 2015.
- [23]. AN EFFICIENT ADIABATIC SWITCHING DESIGN FOR LOW POWER APPLICATIONS, Amit Saxena, Kshitij Shinghal, Deepti Shinghal, International Journal of Engineering Sciences & Emerging Technologies, Vol 8, Issue 6, 2015.

PROFESSIONAL MEMBERSHIPS

- Life Member of ISTE.

PERSONAL DETAILS

Date of Birth	:	06.01.1980
Gender	:	Female
Category	:	General
Marital Status	:	Married

Place: Moradabad

(Deepti Shinghal)