

## CURRICULUM VITAE

### PIYUSH DILIP HEGU

Assistant Professor  
Department of Electronics & Telecommunication  
Jawaharlal Darda Institute of Engineering  
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### Permanent Address

28, Shrikrishna Nagar,  
Vrindavan Society, Darwha Road  
Yavatmal 445001  
Maharashtra  
India

### Educational Degrees

- M.Tech*                      Electronics- VLSI  
Department of Electronics Engineering,  
Priyadarshini College of Engineering,  
Nagpur, Maharashtra, Summer-2014, 66.18 %
- BE*                              Electronics & Telecommunication  
Department of Electronics & Telecommunication Engineering,  
Jawaharlal Darda Institute of Engineering & Technology,  
Yavatmal, Maharashtra, 2009, 62.64 %
- Higher Secondary*      Amolackchand Mahavidyalaya, Yavatmal, 2005, 73.86 %
- Secondary*                      Vivekanand Vidyalaya, Yavatmal, 2003, 64.66 %

### Professional Experience

- **Lecturer**, Dr. Bhausaheb Nandurkar College of Engineering & Technology, Yavatmal (Jul. 2011 – May 2012)
- **Lecturer**, Dr. Bhausaheb Nandurkar College of Engineering & Technology, Yavatmal. (Jun. 2012 – May 2013)
- **Assistant Professor**, Dr. Bhausaheb Nandurkar College of Engineering & Technology, Yavatmal. (Jun. 2013 – May 2014)
- **Assistant Professor**, Dr. Bhausaheb Nandurkar College of Engineering & Technology, Yavatmal. (Jun. 2014 – May 2015)
- **Assistant Professor**, Jawaharlal Darda Institute of Engineering & Technology, Yavatmal (Jun. 2015 – Till date)

## **Reviewer of Journals**

1. Institution of Engineering and Technology (IET) Image Processing, paper number IPR-2014-0157, entitled 'Supervised Bilateral Two-dimensional Locality Preserving Projection Algorithm Based on Gabor Wavelet'.

## **List of Publications**

### **Journal Papers**

Piyush D. Hegu, Mohd. Arshad A. Siddique and Mangesh G. Navalkar. (2013). "Data Scrambler & Descrambler for secure data Communication." International Journal of Engineering Trends & Technology, ISSN: 2278-0181, Vol. 1, Issue 6, pp. 56-61.

Piyush D. Hegu, Manish G. Shingnare and Rini D. Kedarpawar. (2013). "A 3GPP LTE- Advance Turbo Encoder and Turbo Decoder" International Journal of Engineering Trends & Technology, ISSN: 2278-0181, Vol. 1, Issue 6, pp. 121-125.

Piyush D. Hegu, Pradnya J. Suryawanshi and Digamber S. Kale. (2013). "Heterogeneous Face Recognition" International Journal of Engineering Trends & Technology, ISSN: 2278-0181, Vol. 1, Issue 6, pp. 16-21.

### **List of Conference Papers**

Piyush D. Hegu, Mohd. Asad Malnas (2009). "Correction of Simple Contrast Loss in Colour Images" ITECH-2009 International Conference, organized by the Department of Information Technology, Amrutvahini College of Engineering, Sangamner.

Piyush D. Hegu, Pradnya J. Suryawanshi and Ashwin Motikar (2012). "Face Verification Across Age Progression using Discriminative Methods" International Conference on MATLAB Applications in Engineering & Technology, Bangalore, pp. 85-92.

Piyush D. Hegu, Ashwin Motikar (2012). "Application of Virtual Routers in wireless Communication" International Conference on MATLAB Applications in Engineering & Technology, Bangalore, pp. 101-104.

Piyush D. Hegu, Pradnya J. Suryawanshi (2012). "Age Progression Based on Facial Aging Patterns" International Conference on Advances Engg, Science and Management, Nagapattinam, Tamil Nadu, pp. 16-21.

### **UG Projects Guided**

- Design and Implementation of a Wireless Automatic Meter Reading system (Session 2011-12)
- Heterogeneous Face Recognition using Kernel method (Session 2012-13)
- Design and Implementation of 3GPP LTE Turbo Decoder and Turbo Encoder (Session 2013-14)
- Automatic Wheelchair design operated by real time eye tracking system (Session 2014-15)

## Teaching

6XT3 (Introduction To Microprocessor,2014-15, Sem VI)  
5SUL4 (Communication Engineering-I, 2011-12, Sem V)  
7XT2 (Microcontroller and Applications, 2013-14, Sem VII)  
8XT2 (Electronic Circuit Design, 2012-13/14-15, Sem VIII)  
7SUL2 (Microprocessor Peripheral and Microcontroller , 2012-13, Sem VII)  
6SUL2 (Introduction To Microprocessor,2011-12/13-14 Sem VI)

## Administrative

- a. **University Practical Coordinator**, Dept of Electronics & Telecommunication Engineering, DBNCOET, Yavatmal (Session 2011-12/12-13/13-14/14-15)
- b. **AICTE In charge**, DBNCOET, Yavatmal (Session 2014-15)
- c. **Training & Placement Coordinator**, Dept of Electronics & Telecommunication Engineering, DBNCOET, Yavatmal (Session 2014-15)
- d. **Event Coordinator- ANTRATECH-13**, A National Level Technical Symposium organized by DBNCOET, Yavatmal.
- e. **Final Year Class In charge**, Dept of Electronics & Telecommunication Engineering, DBNCOET, Yavatmal (Session 2011-12/12-13/14-15)
- f. **Academic Coordinator**, Dept of Electronics & Telecommunication Engineering, DBNCOET, Yavatmal (Session 2012-13/13-14)
- g. **Event Coordinator**, Sant Gadge Baba Amravati University Youth Festival-2012

## Workshop / STTP Organized

Two weeks ISTE approved (ISTE/Proceedings/STTP-SF/2012-13/810/811) Short Term Training Program on “VLSI Design and Signal Processing” organized by Department of Electronics & Telecommunication Engg, Dr. Bhausaheb Nandurkar College of Engineering, Yavatmal from 29<sup>th</sup> Oct to 8<sup>th</sup> Nov 2012.

## Workshops Attended

- AICTE Sponsored Two days National Workshop on “DSP Design using XILINX System Generator”, Organised by Department of Electronics Engineering, Datta Meghe Institute of Engineering, Technology & Research, Sawangi, Wardha on 18<sup>th</sup> & 19<sup>th</sup> December 2013.
- One day Workshop on “LATEX” organized by Department of Information Technology, Dr. Bhausaheb Nandurkar College of Engineering, Yavatmal on 10<sup>th</sup> Dec 2013.
- ISTE Approved One day Workshop on “Recent Advances in Industrial Management and Technology- The Role of SAP R/3” organized by Department of Mechanical Engineering, Dr. Bhausaheb Nandurkar College of Engineering, Yavatmal on 4<sup>th</sup> Oct 2013.

## Course Attended

Short Term course on “Advance DSP Design Techniques” organized by Indian Institute of Technology, Kharagpur (July 2013)

## **Awards /Achievement**

- PG Project work entitled “ Face Recognition across Age Progression using Discriminative Methods” was selected for 102<sup>nd</sup> Indian Science Congress Conference (Pride of India Expo) held from 3<sup>rd</sup> Jan to 7<sup>th</sup> Jan 2015 at Mumbai through Dr. D. K. Agrawal, Management Council Member, RTMNU Nagpur.
- UG Project work entitled “Correction of Simple Contrast Loss in Colour Images ” has been awarded 1<sup>st</sup> Prize in Software Project demo of TECHNO EXPERTS 09, Technical Festival organized on 25<sup>th</sup> & 26<sup>th</sup> Feb 2009 by PRMITR, Badnera.

## **Membership of Scientific / Engineering Bodies**

1. Associate Member, Institution of Electronics & Telecommunication Engineers (IETE)
2. Board Member of International Journal of Engineering Trends and Technology (IJETT)

## **Personal**

*Date of birth:* October 10, 1987

*Place of birth:* Yavatmal, Maharashtra, India

*Spouse:* Rupal

*Caste:* Lingayat Wani

*Category:* Open